

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **TAKAHASHI, Koji**

Group Art Unit: **2826**

Examiner: **Fazli Erdem**

Filed: **August 26, 2003**

For. **SEMICONDUCTOR MEMORY CAPABLE OF BEING DRIVEN AT LOW
VOLTAGE AND ITS MANUFACTURE METHOD**

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Date: August 28, 2003

Sir:

This is a Preliminary Amendment for the above-captioned Rule 1.53(b) Divisional application. Please amend this Rule 1.53(b) Divisional application as follows: